## **Application Data Sheet**

# **Application Information**

Application Type:: Regular

Subject Matter:: Utility

Suggested Group Art Unit:: N/A

CD-ROM or CD-R?:: None

Sequence submission?:: None

Computer Readable Form (CRF)?:: No

Title:: ASSEMBLIES HAVING STACKED

SEMICONDUCTOR CHIPS AND

METHODS OF MAKING SAME

Attorney Docket Number:: TESSERA 3.0-320

Request for Early Publication?:: No

Request for Non-Publication?:: No

Suggested Drawing Figure:: FIG. 1

Total Drawing Sheets:: 29

Small Entity?:: No

Petition included?:: No

Secrecy Order in Parent Appl.?:: No

#### **Applicant Information**

Applicant Authority Type:: Inventor

Primary Citizenship Country:: Korea, Republic of

Status:: Full Capacity

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Country of Residence:: US

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City of mailing address:: San Jose

State or Province of mailing address:: CA
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Applicant Authority Type:: Inventor
Primary Citizenship Country:: Japan

Status:: Full Capacity

Given Name:: Yoichi
Family Name:: Kubota

City of Residence:: Pleasanton

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City of mailing address:: Pleasanton

State or Province of mailing address:: CA

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**Correspondence Information** 

Correspondence Customer Number:: 038091

Representative Information

Representative Customer Number:: 038091

## **Domestic Priority Information**

Application::	Continuity Type::	Parent Application::	Parent Filing Date::
This Application	An application claiming the benefit under 35 USC 119(e)	60/409,880	09/11/02

## **Assignee Information**

Assignee name:: Tessera, Inc.

Street of mailing address:: 3099 Orchard Drive

City of mailing address:: San Jose

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State or Province of mailing address:: CA

Postal or Zip Code of mailing address:: 95134